



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of

Inderjit Singh et al.

Application No. 10/633,021

Filed: 7/31/2003

For: PAD OVER ACTIVE CIRCUIT SYSTEM
AND METHOD WITH FRAME SUPPORT
STRUCTURE

Examiner: Unassigned

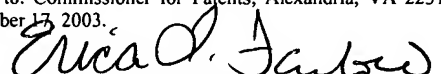
Art Unit: 2811

Date: November 17, 2003

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 on November 17, 2003.

Signed:


Erica L. Farlow

REQUEST FOR CORRECTED FILING RECEIPT

Mail Stop: Office of Initial Patent Examination's Filing Receipt Corrections
Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed is a copy of the Filing Receipt for the above-identified patent application. Please reprint the Filing Receipt as follows and mail the corrected copy to the undersigned.

Change the name of the first inventor from "Inderjit Singh" to
-- Inderjit Singh--. Change the name of the third inventor from "Joseph David Garco" to --Joseph David Greco--.

Because this is a Patent Office typographical error, no fee should be due. However, the Commissioner is authorized to charge any fees that may be due to Deposit Account 50-1351 (Order No. NVIDP235/P000846).

Respectfully submitted,
Silicon Valley IP Group, PC

Kevin J. Zilka
Reg. No. 41,429

P.O. Box 721120
San Jose, CA 95172-1120
(408) 971-2573

Attorney Docket No. NVIDP235/P000846

COPY



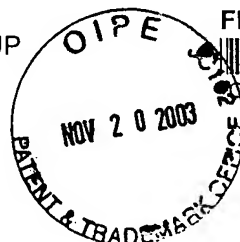
UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/633,021	07/31/2003	2811	1056	NVIDP235/P000846	11	23	6

CONFIRMATION NO. 4737

28875
 SILICON VALLEY INTELLECTUAL PROPERTY GROUP
 P.O. BOX 721120
 SAN JOSE, CA 95172-1120



FILING RECEIPT



C000000011132607*

Date Mailed: 10/29/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Inderjit ~~Inderjit~~ Singh, Saratoga, CA;
 Howard Lee Marks, Gilroy, CA;
 Joseph David ~~Garco~~ Greco, San Jose, CA;

Assignment For Published Patent Application

NVIDIA Corporation;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 10/29/2003

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

Pad over active circuit system and method with frame support structure